## MCM DAV College for Women, Sector – 36A, Chandigarh MonthlyTeaching Plans (Odd Semester/Even Semester) Session–(2022-2023)

Name of the Teacher: Dr. Gurjit Kaur Department: Department of Physics

Class: B.Sc. III (NM and Voc)

**Subject: Electronics and Solid State Devices-1 & 2 (Paper B)** 

Section (s) A, B, Voc

S.No.	(Mo	ate nthly)	Topics to be Covered	Academic Activity Undertaken*	
	From	То	Odd semester		
1.	18 <sup>h</sup> August 2022	31 <sup>th</sup> August 2022	Basic introduction, Concepts of current and voltage sources, Thevenin's theorem, Norton's theorem, Source conversion.	(i) Lecture method (ii) Group discussion (iii) Notes (iv) Numerical Problems	
2.	1 <sup>th</sup> Sept. 2022	30 <sup>th</sup> Sept. 2022	CRO, Block diagram, construction and principle of working, Use of CRO for frequency, time period, special features of dual trace, phase measurements. Energy band diagrams in semiconductors, Direct and indirect semiconductors, Formula to calculate Position of Fermi level in p and n semiconductors, Barrier formation, energy band diagram of p-n junction, Formula for Depletion width, Qualitative ides of current flow mechanism in forward and reverse biased diode, V-I characteristics.	(i) Lecture method (ii) Group discussion (iii) Notes (iv) Numerical Problems (v) online material	
3.	1 <sup>st</sup> Oct. 2022	31 <sup>st</sup> Oct. 2022	Static and dynamic resistance, Depletion and diffusion capacitance, zener diode, LED, photodiode and solar cell, Diode circuits, Clipping circuits, Rectification: half wave, full wave and bridge rectifiers, filter circuits (C, LC and $\pi$ filters). Rectification efficiency and ripple factor in LC	(i) Lecture method (ii) PPt (iii) Group discussion (iv) Notes (v) Numerical Problems (vi) online material	

			filter, voltage regulation circuit using zener diode, voltage multiplier circuits.		
4.	1 <sup>st</sup> Nov. 2022	25 <sup>th</sup> Nov. 2022	Bipolar Junction transistors: Structure and working, different currents in transistor, switching action. Characteristics of CB, CE and CC configurations, Active, cutoff and saturation regions, Load line analysis of transistors, Q-point, Transistor biasing and stabilization of operating point, fixed bias, collector to base bias, bias circuit with emitter resistor, voltage divider biasing circuit. Working ans analysis of CE amplifier using h-parameters, current, voltage and power gain, input and output impedance. Class A, B and C amplifiers.	(i) (ii) (iii) (iv) (v) Proble	Lecture method PPt Group discussion Notes Numerical ems
1	1 crd v	2.1rd r	Even Semester		T
1.	16 <sup>rd</sup> Jan. 2023	31 <sup>rd</sup> Jan. 2023	Structure and working of JEFT, characteristics, drain and transconductance curve, FET amplifier and its voltage gain, Structure and working of MOSFET, Feed back in amplifiers, voltage gain of negative feedback amplifier, advantages of negative voltage feedback, negative current feedback circuit, emitter follower.	(i) (ii) (iii) (iv) (v) Proble	Lecture method PPt Group discussion Notes Numerical ems
2.	1 <sup>rd</sup> Feb. 2023	28 <sup>th</sup> Feb. 2023	Theory of sinusoidal oscillations, loop gain and phase, Lead-lag RC circuit, Wein bridge oscillator. Barkhausen criterion of sustained oscillations, positive feedback amplifier, LC oscillators, Colpitts and Hartley oscillators. Operational amplifier (black box approach): Characteristics of ideal and practical opamp 741, openloop and closed-loop gain, characteristics and applications inverting and non-inverting amplifiers, adder, subtractor, differentiator and integrator, Comparator, Timer IC555, pin diagram and its applications as astable and monostable multivibrator.	(i) (ii) (iii) (iv) (v) Proble	Lecture method PPt Group discussion Notes Numerical

	1 <sup>st</sup>	31 <sup>st</sup>	Analog and digital circuits, binary	(i) Lecture method
2.	March,2023	March,2023	numbers, decimal to binary	(ii) PPt
			conversions, AND, OR, NOT	(iii) Group discussion
			gates, NAND NOR gates as	(iv) Notes
			universal gates, XOR and XNOR	(v) Numerical
			gates. De Morgan's theorem,	Problems
			Simplification of logic circuit	(vi) online material
			using Boolean algebra, Minterms	
			and Maxterms, Conversion of a	
			truth table into an equivalent logic	
			circuit by Sum of products	
			method.	
3.	1 <sup>st</sup>	29 <sup>st</sup>	Analog and digital communication	(i) Lecture method
	April,2023	April,2023	systems, Amplitude and	(ii) Group discussion
			Frequency modulation, Power in	(iii) Notes
			AM wave, generation and	(iv) Numerical
			detection, Brief account of	Problems
			Satellite communication, Sky-	
			wave communication, and mobile	
			communication.	